



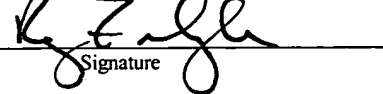
2132

#7

PATENT
068167.0107

RECEIVED
APR 04 2002
Technology Center 2100

Pursuant to 37 C.F.R. § 1.8, I hereby certify that I have information and a reasonable basis for belief that this correspondence will be deposited as first-class mail with the United States Postal Service in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231 on March 27, 2002.


Signature

3-27-02
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Giles, et al.

Serial No.: 09/809,731

Filed: March 15, 2001

Invention: Method for Hybrid Processing of
Software Instructions of an
Emulated Computer System

Commissioner for Patents
Washington, D.C. 20231

§
§
§
§
§
§
§
§
§
§
§

Group No.: 2132

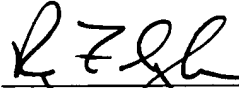
Examiner: Not Yet Assigned

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the art listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. A copy of the cited art is enclosed for the convenience of the Examiner. Pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

The information disclosure statement submitted herewith is being submitted before the mailing of the first office action on the merits. Applicants believe that no fee is required. If a fee is required, please charge any fee to Deposit Account No. 02-0383 of Baker Botts L.L.P.

Respectfully submitted,



Roger Fulghum
Registration No. 39,678

Baker Botts L.L.P.
One Shell Plaza
910 Louisiana
Houston, TX 77002-4995
(713) 229-1707
Attorney Docket No.: 068167.0107

Date: March 27, 2002



#7

PTO-1449		Application No. 09/809,731		Applicant(s) Giles, et al.				
		Docket Number 068167.0107		Group Art Unit 2132		Filing Date 3/15/01		
Information Disclosure Citation in an Application								
U.S. PATENT DOCUMENTS								
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	A	5,815,686	9/29/98	Earl, et al.	395	500	9/12/96	
	B	4,779,188	10/18/88	Gum et al.	364	200	10/19/87	
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES NO	
	C	EP 0 645 701 A2	26/09/94	EPO	G06F	9/455	X	
NON-PATENT DOCUMENTS								
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)						
	D	PCT International Search Report in International Application No. PCT/US 01/22276, International filing date 16/07/01, mail date 07/03/02.						
	E	Traut E, "Building the Virtual PC," <i>Byte</i> , McGraw-Hill Inc., Vol. 22, No. 11, pp. 51-52, 1 November 1997.						
	F	"Intel386 DX Microprocessor," <i>Intel</i> , pp. 32-58, 31 December 1995.						
	G	"Macintosh and Technology: Changing Chips in the Middle of the Stream, or Apple Takes a Risc," URL: www.btech.co/changingchips.html , paragraphs '0006!'-'0007!', retrieved 12/10/01.						
	H	"M68040 User's Manual," Motorola, Inc., Chapter 3, copyright 1990, revised 1992, 1993.						
	I	Osisek DL et al., "ESA/390 Interpretive-Execution Architecture, Foundation for VM/ESA," <i>IBM Systems Journal</i> , Vol. 30, No. 1, pp. 34-51, 1991.						
	J	Shang Rong Tsai et al., "On the Architectural Support for Logical Machine Systems," <i>Microprocessing and Microprogramming</i> , Vol. 22, No. 2, pp. 81-96, February 1988.						
EXAMINER					DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.								

U.S. Patent and Trademark Office

HOU03:838713